

What is claimed is:

1. A method of determining a checksum of at least a portion of a packet,
the method comprising:

accessing a checksum of the at least the portion of the packet;

adjusting the checksum of the at least the portion of the packet based on
values of a subset of the at least the portion of the packet before and after
modification of the subset.

2. The method of claim 1, wherein the packet comprises a Transmission
Control Protocol (TCP) segment.

3. The method of claim 2, wherein the at least the portion of the packet
comprises the TCP segment payload.

4. The method of claim 2, wherein the adjusting the checksum comprises:
subtracting a checksum of the subset of the segment before the
modification; and

adding a checksum of the subset of the segment after the modification.

5. The method of claim 2, further comprising performing the adjusting of
the checksum after each of a series of modifications of the segment.

6. The method of claim 1, further comprising:
receiving a request to access the subset of the at least the portion of the packet from an application;
caching the subset;
receiving a request to write data to the cached subset; and
performing the adjusting.
7. The method of claim 6, wherein the application comprises an application to parse and modify data included in the packet payload.
8. The method of claim 1, wherein the adjusting comprises executing program instructions at an engine of network processor having multiple engines.
9. The method of claim 8, wherein the engines comprise engines individually providing multiple threads having associated program counters.
10. The method of claim 1, further comprising:
storing an identifier of the packet and a set of data associated with the identifier, the set of data including data identifying the checksum of the at least the portion of the packet.
11. The method of claim 10, wherein the set of data includes data

identifying a cached copy of the subset of the at least the portion of the packet.

12. The method of claim 10, wherein the storing the identifier comprises storing the identifier in a content addressable memory (CAM).

13. A computer program product, disposed on a computer readable medium, to determine a checksum of at least a portion of a packet, the program the program including instructions for causing programmable circuitry to:

access a checksum of the at least the portion of the packet;

adjust the checksum of the at least the portion of the packet based on values of a subset of the at least the portion of the packet before and after modification of the subset.

14. The program of claim 13, wherein the packet comprises a Transmission Control Protocol (TCP) segment.

15. The program of claim 14, wherein the at least the portion of the packet comprises the TCP segment payload.

16. The program of claim 14, wherein the instructions to cause the programmable circuitry to adjust the checksum comprise instructions to cause the programmable circuitry to:

subtract the checksum of the subset of the segment before the modification;

and

add the checksum of the subset of the segment after the modification.

17. The program of claim 14, further comprising instructions for causing the programmable circuitry to perform the adjusting of the checksum after each of a series of modifications of the segment payload.

18. The program of claim 13, further comprising instructions for causing the programmable circuitry to:

receive a request to access the subset of the at least the portion of the packet from an application;

cache the subset;

receive a request to write data to the cached subset; and

perform the adjusting.

19. The program of claim 13, wherein the instructions comprise instructions in an instruction set of an engine of network processor having multiple multi-threaded engines integrated on a single die.

20. The program of claim 13, further comprising instructions for causing the programmable circuitry to:

store an identifier to the packet and a set of data associated with the identifier, the set of data including data identifying the checksum of the at least the

portion of the packet.

21. The program of claim 20, wherein the set of data includes data identifying a cached copy of the subset of the at least the portion of the packet.

22. The program of claim 20, wherein the instructions to cause the programmable circuitry to store the identifier comprise instructions to cause the programmable circuitry to store the identifier in a content addressable memory (CAM).

23. A component, comprising:

an input interface;

an output interface;

logic to

access a checksum of at least a portion of a packet received via the input interface;

adjust the checksum of the at least the portion of the packet based on values of a subset of the at least the portion of the packet before and after modification of the subset.

24. The component of claim 23, wherein the packet comprises a Transmission Control Protocol (TCP) segment.

25. The component of claim 23, wherein the logic further comprises logic to receive a request to access the subset of the at least the portion of the packet from an application;

cache the subset;

receive a request to write data to the cached subset; and

perform the adjusting.

26. The component of claim 23, further comprising logic to:

store an identifier to the packet and a set of data associated with the identifier, the set of data including data identifying the checksum of the at least the portion of the packet, the set of data including data identifying a cached copy of the subset of the at least the portion of the packet.

27. A system, comprising:

at least one processor; and

at least one network interface operationally coupled to the processor, the network interface comprising:

at least one PHY;

at least one framer; and

at least one Transmission Control Protocol (TCP) Offload Engine (TOE), the Offload Engine comprising circuitry to determine a checksum of at least a portion of a TCP segment, the circuitry to:

access a checksum of the at least the portion of the segment;

adjust the checksum of the at least the portion of the packet based on values of a subset of the at least the portion of the packet before and after modification of the subset.

28. The system of claim 27, wherein the framer comprises an Ethernet framer.

29. The program of claim 27, wherein the circuitry comprises an engine of network processor having multiple multi-threaded engines integrated on a single die.

30. The program of claim 27, wherein the circuitry stores a reference to the packet and a set of data associated with the reference, the set of data including data identifying the checksum of the at least the portion of the packet, the set of data including data identifying a cached copy of the subset of the at least the portion of the packet.